



HAMRE, SCHUMANN, MUELLER & LARSON, P.C.

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December 21, 2007

TO:

Mail Stop: AMENDMENT Examiner: David J. Goodwin Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

FROM: Douglas P. Mueller

OUR REF: 10873.1744USWO

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Title of Document:

AMENDMENT AND RESPONSE (4 pages)

Applicant: Serial No.: Nanai et al. 10/553,860

App. Filed:

October 20, 2005

Group Art No.: 2818

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DEC 2 1 2007

S/N 10/553,860

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Nanai et al.

Examiner:

David J. Goodwin

Serial No.:

10/553,860

Group Art Unit:

2818

Filed:

October 20, 2005

Docket No.:

10873.1744USWO

Title:

FIELD EFFECT TRANSISTOR, ELECTRICAL ELEMENT ARRAY, AND

MANUFACTURING METHOD FOR THE SAME

CERTIFICATE UNDER 37 CFR 1,6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on December

21, 2007.

Mail Stop: AMENDMENT Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

AMENDMENT AND RESPONSE

Dear Sir:

In response to the Office Action mailed September 21, 2007, please amend the above-identified application as follows:

Amendments to the Claims are shown in the listing of claims that begins on page 2 of this document.

Remarks/Arguments begin on page 4 of this document.